

ABSTRACT OF THE DISCLOSURE

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A structure and driving method of a plasma display panel is provided, in which an amount of priming particles within a discharge cell increases to reduce discharge lag of address discharge. The structure of the plasma display panel includes a plurality of sustain electrode pairs successively formed on an upper electrode, a plurality of common electrodes formed one by one between a pair of the sustain electrodes, and a dielectric layer formed on the substrate to deposit the sustain electrodes and the common electrodes. The method for driving the plasma display panel includes the steps of applying a common pulse, which is periodically turned on/off, to the common electrodes, applying a scan pulse to one of a pair of the sustain electrodes, and applying an address pulse to the address electrodes when the scan pulse is applied to the one sustain electrode. Thus, since discharge conditions within the discharge cell can be improved, discharge lag less occurs than the related art plasma display panel.